

VLSI Fabrication Technology

1. S. K. Gandhi, *VLSI Fabrication Principles : Silicon and Gallium Arsenide*, Second Edition, Wiley, 1994.
2. G. S. May and S. M. Sze, *Fundamentals of Semiconductor Fabrication*, Wiley, 2004.
3. J. D. Plummer, M. D. Deal and P. B. Griffin, *Silicon VLSI Technology : Fundamentals, Practice and Modeling*, Pearson/PH, 2001.
4. P. Van Zant, *Microchip Fabrication : A Practical Guide to Semiconductor Processing*, Fifth Edition, McGraw-Hill, 2004. (Cheap Edition)

Semiconductor Devices, Device Modeling and Simulation, SPICE

5. B. L. Anderson and R. L. Anderson, *Fundamentals of Semiconductor Devices*, McGraw-Hill, 2004.
6. D. Foty, *MOSFET Modeling with SPICE : Principles and Practices*, Prentice-Hall, 1997.
7. W. Liu, *MOSFET Models for SPICE Simulation Including BSIM3v3 and BSIM4*, Wiley, 2001.
8. R. S. Muller, T. I. Kamins and M. Chan, *Device Electronics for Integrated Circuits*, Third Edition, Wiley, 2003. (Cheap Edition)
9. G. W. Roberts and A. S. Sedra, *SPICE*, Second Edition, OUP, 1996. (Cheap Edition)
10. A. S. Sedra and K. C. Smith, *Microelectronic Circuits*, Fifth Edition, OUP, 2003. (Cheap Edition)
11. B. G. Streetman and S. Banerjee, *Solid State Electronic Devices*, Sixth Edition, PHI, 2005. (Cheap Edition)
12. A. Vladimirescu, *The SPICE Book*, Wiley, 1994.

VLSI Design, VLSI Subsystem Design

13. R. J. Baker, *CMOS : Circuit Design, Layout and Simulation*, Second Edition, Wiley, 2004. (Cheap Edition ?)
14. J-P. Deschamps, G. J. A. Bioul and G. D. Sutter, *Synthesis of Arithmetic Circuits : FPGA, ASIC and Embedded Systems*, Wiley, 2006.
15. S-M. Kang and Y. Leblebici, *CMOS Digital Integrated Circuits : Analysis and Design*, Third Edition, McGraw-Hill, 2002. (Cheap Edition)
16. C. Mead and L. Conway, *Introduction to VLSI Systems*, Addison-Wesley, 1980. (Out of Print)

17. J. M. Rabaey, A. Chandrakasan and B. Nikolic, *Digital Integrated Circuits : A Design Perspective*, Second Edition, Pearson/PH, 2003. (Cheap Edition)
18. J. P. Uyemura, *Introduction to VLSI Circuits and Systems*, Wiley, 2001.
19. J. P. Uyemura, *CMOS Logic Circuit Design*, Second Edition, Kluwer, 1999. (Cheap Edition)
20. L. Wanhammar, *DSP Integrated Circuits*, AP, 1999.
21. N. Weste, D. Harris and S. Banerjee, *CMOS VLSI Design : A Circuits and Systems Perspective*, Third Edition, Pearson/AW, 2005. (Cheap Edition)
22. W. Wolf, *Modern VLSI Design : Systems-on-Chip Design*, Third Edition, Pearson/PH, 2002. (Cheap Edition)

ASIC Design, FPGA Design, Reconfigurable Computing

23. U. Meyer-Baese, *Digital Signal Processing with Field Programmable Gate Arrays*, Springer, 2001.
24. J. V. Oldfield and R. C. Dorf, *FPGAs : Reconfigurable Logic for Rapid Prototyping and Implementation of Digital Systems*, Wiley, 1995.
25. M. J. S. Smith, *Application Specific Integrated Circuits*, Pearson/AW, 1997. (Cheap Edition)
26. W. Wolf, *FPGA-based System Design*, PH/Pearson, 2004. (Cheap Edition)

Analog IC Design, Mixed Signal Design, RF IC Design

27. A. Agarwal and J. Lang, *Foundations of Analog and Digital Electronic Circuits*, Elsevier/MK, 2005.
28. P. E. Allen and D. R. Holberg, *CMOS Analog Circuit Design*, Second Edition, OUP, 2002.
29. C. Enz and E. Vittoz, *Charge-Based MOS Transistor Modeling : The EKV Model for Low-Power and RF IC Design*, Wiley, 2006.
30. P. Gray, P. J. Hurst, S. H. Lewis and R. Meyer, *Analysis and Design of Analog Integrated Circuits*, Fourth Edition, Wiley, 2001. (Cheap Edition)
31. R. Gregorian and G. C. Temes, *Analog MOS Integrated Circuits for Signal Processing*, Wiley, 1986/2002. (Cheap Edition)
32. A. Hastings, *The Art of Analog Layout*, Second Edition, Prentice-Hall, 2006.
33. D. A. Johns and K. Martin, *Analog Integrated Circuit Design*, Wiley, 1997.

34. K. R. Laker and W. M. C. Sansen, *Design of Analog Integrated Circuits and Systems*, McGraw-Hill, 1994. (Cheap Edition)
35. T. H. Lee, *Design of CMOS Radio Frequency Integrated Circuits*, Second Edition, CUP, 2004. (Cheap Edition ?)
36. B. Razavi, *Design of Analog CMOS Integrated Circuits*, McGraw-Hill, 2000.
37. R. J. van de Plassche, *Integrated A-D and D-A Converters*, Second Edition, Springer/Kluwer, 2003. (Cheap Edition)

VLSI Systems Architecture, Computer Architecture, DSP Architecture

38. G. A. Blaauw and F. P. Brooks, *Computer Architecture : Concepts and Evolution*, Pearson/AW, 1997.
39. V. P. Heuring and H. F. Jordan, *Computer Systems Design and Architecture*, Second Edition, PHI, 2004. (Cheap Edition)
40. D. A. Patterson and J. L. Hennessy, *Computer Architecture : A Quantitative Approach*, Fourth Edition, Elsevier/MK, 2006. (To Appear)
41. D. A. Patterson and J. L. Hennessy, *Computer Architecture : A Quantitative Approach*, Third Edition, Elsevier/MK, 2002. (Cheap Edition)
42. D. A. Patterson and J. L. Hennessy, *Computer Organization and Design : Hardware/Software Interface*, Third Edition, Elsevier/MK, 2004. (Cheap Edition)
43. W. Stallings, *Computer Organization and Architecture : Designing for Performance*, Seventh Edition, Pearson/PH, 2006. (Cheap Edition)
44. A. S. Tanenbaum, *Structured Computer Organization*, Fifth Edition, Pearson/PH, 2006. (Cheap Edition)
45. W. Wolf, *Modern VLSI Design : Systems-on-Chip Design*, Third Edition, Pearson/PH, 2002. (Cheap Edition)
46. S. M. Kuo and W-S. S. Gan, *Digital Signal Processors : Architectures, Implementations, and Applications*, Prentice-Hall, 2004.
47. P. Lapsley, J. Bier, A. Shoham and E. Lee, *DSP Processor Fundamentals : Architectures and Features*, IEEE Press, 1997.
48. K. K. Parhi, *VLSI Digital Signal Processing Systems : Design and Implementation*, Wiley, 1999.
49. P. Pirsch, *Architectures for Digital Signal Processing*, Wiley, 1998.
50. A. Singh and S. Srinivasan, *Digital Signal Processing Implementations*, Thomson, 2004.

VHDL, Verilog and HDL-Based Design

51. J. Armstrong and F. G. Gray, *VHDL Design Representation and Synthesis*, Second Edition, Prentice-Hall, 2000.
52. P. J. Ashenden, *The Designer's Guide to VHDL*, Second Edition, Elsevier/MK, 2001. (Cheap Edition)
53. J. Bhasker, *A VHDL Primer*, Third Edition, Prentice-Hall, 1999. (Cheap Edition)
54. J. Bhasker, *A VHDL Synthesis Primer*, Second Edition, Star Galaxy, 1998. (Cheap Edition)
55. S. Ghosh, *Hardware Description Languages : Concepts and Principles*, PHI, 2000. (Cheap Edition)
56. S. Sjöholm and L. Lindh, *VHDL for Designers*, Prentice-Hall, 1997.
57. S. Yalamanchili, *Introductory VHDL : From Simulation to Synthesis*, Prentice-Hall, 2000. (Cheap Edition)
58. S. Yalamanchili, *VHDL : A Starter's Guide*, Second Edition, Prentice-Hall, 2004. (Cheap Edition ?)
59. M. G. Arnold, *Verilog Digital Computer Design : Algorithms to Hardware*, Prentice-Hall, 1999.
60. J. Bhasker, *A Verilog HDL Primer*, Third Edition, Star Galaxy, 2005. (Cheap Edition)
61. J. Bhasker, *Verilog HDL Synthesis : A Practical Primer*, Star Galaxy, 1998. (Cheap Edition)
62. D. J. Lilja and S. S. Sapatnekar, *Designing Digital Computer Systems with Verilog*, CUP, 2004.
63. S. Palnitkar, *Verilog HDL : A Guide to Digital Design and Synthesis*, Second Edition, Prentice-Hall, 2003. (Cheap Edition)
64. D. E. Thomas and P. R. Moorby, *The Verilog Hardware Description Language*, Fourth Edition, Kluwer, 1998.
65. R. M. Zeidman, *Verilog Designer's Library*, Prentice-Hall, 1999.
66. J. Bhasker, *A SystemC Primer*, Second Edition, Star Galaxy, 2004.
67. S. Sutherland, S. Davidmann and P. Flake, *SystemVerilog for Design : A Guide to Using SystemVerilog for Hardware Design and Modeling*, Second Edition, Springer, 2006.

VLSI/IC CAD and Algorithms, High-Level Synthesis

68. M. D. Birnbaum, *Essential Electronic Design Automation (EDA)*, Prentice-Hall, 2003.
69. G. De Micheli, *Synthesis and Optimization of Digital Circuits*, McGraw-Hill, 1994. (Cheap Edition)
70. S. H. Gerez, *Algorithms for VLSI Design Automation*, Wiley, 1998.
71. S. M. Sait and H. Youssef, *VLSI Physical Design Automation : Theory and Practice*, WSP, 1999. (Cheap Edition)
72. S. M. Sait and H. Youssef, *Iterative Computer Algorithms with Applications in Engineering : Solving Combinatorial Optimization Problems*, Wiley/IEEE, 2000.
73. N. Sherwani, *Algorithms for VLSI Physical Automation*, Third Edition, Kluwer, 1998. (Cheap Edition)
74. Proceedings of the IEEE (Special Issue on VLSI CAD Tools), February, 1990.

Hardware/Software Codesign, Embedded Systems

75. D. D. Gajski, F. Vahid, S. Narayan and J. Gong, *Specification and Design of Embedded Systems*, Prentice-Hall, 1994.
76. P. Marwedel, *Embedded System Design*, Springer, 2006.
77. F. Mayer-Lindenberg, *Dedicated Digital Processors : Methods in Hardware/Software Co-Design*, Wiley, 2004.
78. T. Noergaard, *Embedded Systems Architecture : A Comprehensive Guide for Engineers and Programmers*, Elsevier/Newnes, 2005.
79. M. Srivastava, and V. Raghunathan, *Embedded Computing : A Systems Approach*, Springer, 2007. (To Appear in June, 2007)
80. J. Staunstrup and W. Wolf, *Hardware/Software Co-Design : Principles and Practices*, Kluwer, 1997.
81. F. Vahid and T. Givargis, *Embedded System Design : A Unified Hardware/Software Introduction*, Wiley, 2002. (Cheap Edition)
82. W. Wolf, *Computers as Components : Principles of Embedded Computer System Design*, Second Edition, Elsevier/MK, 2005. (Cheap Edition)
83. Proceedings of the IEEE (Special Issue on HW/SW Codesign), March, 1997.

VLSI Testing, Testability and Formal Verification

84. M. Abramovici, M. A. Breuer and A. D. Friedman, *Digital Systems Testing and Testable Design*, Revised Edition, IEEE Press, 1998. (Cheap Edition)

85. M. L. Bushnell and V. D. Agrawal, *Essentials of Electronic Testing for Digital, Memory and Mixed Signal VLSI Circuits*, Springer/Kluwer, 2000.
86. N. K. Jha and S. Gupta, *Testing of Digital Systems*, CUP, 2003.
87. A. Miczo, *Digital Logic Testing and Simulation*, Second Edition, Wiley, 2003.
88. L-T. Wang, C-W. Wu and X. Wen, *VLSI Test Principles and Architectures*, Elsevier/MK, 2006. (To Appear)
89. G. D. Hachtel and F. Somenzi, *Logic Synthesis and Verification Algorithms*, Springer, 2006.

Low-Power Design Techniques

90. D. Binkley, *Optimizing Analog CMOS Design*, Wiley, 2006.
91. A. P. Chandrakasan and R. W. Broderson, *Low Power CMOS Design*, IEEE Press, 1998.
92. C. Enz and E. Vittoz, *Charge-Based MOS Transistor Modeling : The EKV Model for Low-Power and RF IC Design*, Wiley, 2006.
93. J. B. Kuo and J-H. Lou, *Low Voltage CMOS VLSI Circuits*, Wiley, 1998.
94. B. Wong, A. Mittal, Y. Cao, G. W. Starr, *Nano-CMOS Circuit and Physical Design*, Wiley, 2004.
95. K-S. Yeo and K. Roy, *Low Voltage, Low Power VLSI Subsystems*, McGraw-Hill, 2004.

VLSI Interconnects and Analysis

96. H. B. Bakoglu, *Circuits, Interconnections and Packaging for VLSI*, Addison-Wesley, 1990. (Out of Print)
97. C-K. Cheng, J. Lillis, S. Lin and N. Chang, *Interconnect Analysis and Synthesis*, Wiley, 1999.
98. A. Goel, *High Speed VLSI Interconnections : Modeling, Analysis and Simulation*, Wiley, 1994.
99. S. H. Hall, G. W. Hall and J. A. McCall, *High-Speed Digital System Design : A Handbook of Interconnect Theory and Design Practices*, Wiley/IEEE, 2000.
100. R. P. Singh, *Signal Integrity Effects in Custom IC and ASIC Designs*, Wiley/IEEE, 2001.
101. M. Celik, L. Pileggi and A. Odabasioglu, *IC Interconnect Analysis*, Kluwer, 2002.

System Design, System Architecture

102. W. J. Dally and J. W. Poulton, *Digital Systems Engineering*, CUP, 1998.
103. W. J. Dally and B. P. Towles, *Principles and Practices of Interconnection Networks*, Elsevier/MK, 2003.
104. J. Di Giacomo, *Digital Bus Handbook*, McGraw-Hill, 1990. (Out of Print)
105. H. W. Johnson and M. Graham, *High Speed Digital Design : A Handbook of Black Magic*, Prentice-Hall, 1993.
106. Y. N. Patt and S. J. Patel, *Introduction to Computing Systems : From Bits and Gates to C and Beyond*, Second Edition, McGraw-Hill, 2004.
107. R. J. Tocci, N. S. Widmer and G. L. Moss, *Digital Systems : Principles and Applications*, Tenth Edition, Prentice-Hall, 2006. (Cheap Edition)

Digital Logic Design

108. D. D. Gajski, *Principles of Digital Design*, Prentice-Hall, 1997.
109. E. J. McCluskey, *Logic Design Principles : With Emphasis on Testable Semicustom Circuits*, Prentice-Hall, 1986. (Out of Print)
110. S. H. Unger, *Essence of Logic Circuits*, Second Edition, Wiley/IEEE, 1998.
111. F. Vahid, *Digital Design*, Wiley, 2006.
112. J. F. Wakerly, *Digital Design Principles and Practices*, Fourth Edition, Prentice-Hall, 2005. (Cheap Edition ?)

Linux/Unix System Administration

1. M. Bishop, *Introduction to Computer Security*, Addison-Wesley, 2004.
2. W. R. Cheswick, S. M. Bellovin and A. D. Rubin, *Firewalls and Internet Security : Repelling the Wily Hacker*, Second Edition, Addison-Wesley, 2003.
3. A. Frisch, *Essential System Administration*, Third Edition, O'Reilly, 2002. (Cheap Edition)
4. B. W. Kernighan and R. Pike, *The Unix Programming Environment*, PHI, 1982. (Cheap Edition)
5. T. A. Limoncelli and C. Hogan, *The Practice of System and Network Administration*, Pearson/AW, 2002. (Cheap Edition)
6. E. Nemeth, G. Snyder and T. Hein, *Linux Administration Handbook*, PHI, 2002. (Cheap Edition)
7. M. G. Sobell, *A Practical Guide to Linux Commands, Editors, and Shell Programming*, Pearson/PH, 2005. (Cheap Edition ?)
8. S. Garfinkel, G. Spafford and A. Schwartz, *Practical UNIX and Internet Security*, Third Edition, O'Reilly, 2003.

VLSI Fabrication Technology

1. A. R. Alvarez, *BiCMOS Technology and Applications*, Second Edition, Kluwer, 1993.
2. S. A. Campbell, *The Science and Engineering of Microelectronic Fabrication*, Second Edition, OUP, 2001.
3. B. G. Eynon and B. Wu, *Photomask Fabrication Technology*, McGraw-Hill, 2005.
4. S. Franssila, *Introduction to Microfabrication*, Wiley, 2004.
5. H. Geng, *Semiconductor Manufacturing Handbook*, McGraw-Hill, 2005.
6. R. C. Jaeger, *Introduction to Microelectronics Fabrication*, Second Edition, Pearson/AW/PH, 2002.
7. G. S. May and C. J. Spanos, *Fundamentals of Semiconductor Manufacturing and Process Control*, Wiley, 2006.
8. D. Nagchoudhuri, *Principles of Microelectronics Technology*, Wheeler (India), 1998. (Cheap Edition)
9. S. M. Sze, *VLSI Technology*, Second Edition, McGraw-Hill, 1988. (Cheap Edition)
10. S. M. Sze, *Semiconductor Devices : Physics and Technology*, Second Edition, Wiley, 2002.
11. Y. P. Tsividis, *Mixed Analog-Digital VLSI Devices and Technology : An Introduction*, McGraw-Hill, 1996.

Semiconductor Devices, Device Modeling and Simulation, SPICE

12. N. Arora, *MOSFET Models for VLSI Circuit Simulation*, Springer, 1993.
13. D. Bell, *Electronic Devices and Circuits*, Fourth Edition, PHI, 1999. (Cheap Edition)
14. T. F. Bogart, J. S. Beasley and G. Rico, *Electronic Devices and Circuits*, Sixth Edition, Pearson/PH, 2004.
15. K. F. Brennan, *Introduction to Semiconductor Devices*, CUP, 2005.
16. K. F. Brennan and A. S. Brown, *Theory of Modern Electronic Semiconductor Devices*, Wiley, 2002.
17. J. Cathey, *Schaum's Outline of Electronic Devices and Circuits*, Second Edition, McGraw-Hill, 2002.
18. Y. Cheng and C. Hu, *MOSFET Modeling and BSIM3 User's Guide*, Kluwer, 1999.
19. S. Dimitrijevic, *Understanding Semiconductor Devices*, OUP, 2000.
20. N. Dasgupta and A. Dasgupta, *Semiconductor Devices : Modelling and Technology*, PHI, 200x. (Cheap Edition)

21. T. A. Fjeldly, T. Ytterdal and M. Shur, *Introduction to Device Modeling and Circuit Simulation*, Wiley, 1997.
22. C. G. Fonstad, *Microelectronic Devices and Circuits*, McGraw-Hill, 1994. (Cheap Edition)
23. A. S. Grove, *Physics and Technology of Semiconductor Devices*, Wiley, 1967. (Cheap Edition)
24. K. Kano, *Semiconductor Devices*, PHI, 200x. (Cheap Edition)
25. R. Kielkowski, *Inside SPICE*, Second Edition, McGraw-Hill, 1998.
26. K. Lee, M. Shur, T. A. Fjeldly and T. Ytterdal, *Semiconductor Device Modeling for VLSI*, Prentice-Hall, 1993.
27. G. Massobrio and P. Antognetti, *Semiconductor Device Modeling with SPICE*, Second Edition, McGraw-Hill, 1998.
28. D. Nagchoudhuri, *Microelectronic Devices*, Pearson, 2001. (Cheap Edition)
29. K. K. Ng, *Complete Guide to Semiconductor Devices*, Second Edition, Wiley/IEEE, 2002. (Cheap Edition ?)
30. E. H. Nicollian and J. R. Brews, *MOS Physics and Technology*, Wiley, 1982/2002. (Cheap Edition)
31. D. J. Roulston, *An Introduction to the Physics of Semiconductor Devices*, OUP, 1998.
32. W. R. Runyan and T. J. Shaffner, *Semiconductor Measurements and Instrumentation*, Second Edition, McGraw-Hill, 1998.
33. M. Satyam and K. Ramkumar, *Fundamentals of Electronic Devices*, Wiley, 1990. (Cheap Edition)
34. D. K. Schroder, *Semiconductor Material and Device Characterization*, Second Edition, Wiley, 1998.
35. M. Shur, *Introduction to Electronic Devices*, Wiley, 1995.
36. M. Shur, *Physics of Semiconductor Devices*, Prentice-Hall, 1990. (Cheap Edition)
37. S. M. Sze, *Modern Semiconductor Device Physics*, Wiley, 1998.
38. S. M. Sze, *Semiconductor Devices : Physics and Technology*, Second Edition, Wiley, 2002. (Cheap Edition)
39. S. M. Sze and K. K. Ng, *Physics of Semiconductor Devices*, Third Edition, Wiley, 2006.
40. Y. Taur and T. H. Ning, *Fundamentals of Modern VLSI Devices*, CUP, 1998.
41. Y. P. Tsividis, *Operation and Modeling of the MOS Transistor*, McGraw-Hill, 1987.

42. P. W. Tuinenga, *SPICE : A Guide to Circuit Simulation and Analysis Using P-SPICE*, Third Edition, Prentice-Hall, 1995. (Cheap Edition)
43. R. K. Watts, *Submicron Integrated Circuits*, Wiley, 1989.
44. J. S. Yuan and J. J. Liou, *Semiconductor Device Physics and Simulation*, Plenum, 1998.

VLSI Design, VLSI Subsystem Design

45. J. E. Ayers, *Digital Integrated Circuits : Analysis and Design*, CRC Press, 2003.
46. S. H. K. Embabi, A. Bellaouar and M. I. Elmasry, *Digital BiCMOS Integrated Circuit Design*, Kluwer, 1993.
47. K. Bernstein, K. M. Carrig, C. M. Durham, P. R. Hansen, D. Hogenmiller, E. J. Nowak and N. J. Rohrer, *High Speed CMOS Design Styles*, Kluwer, 1998.
48. K. Eshraghian, D. A. Pucknell and S. Eshraghian, *Essentials of VLSI Circuits and Systems*, PHI, 2005. (Cheap Edition)
49. L. A. Glasser and D. W. Dobberpuhl, *The Design and Analysis of VLSI Circuits*, Addison-Wesley, 1985. (Out of Print)
50. J. Handy, *The Cache Memory Book*, Second Edition, Elsevier/MK, 1998.
51. D. Harris, *Skew-Tolerant Circuit Design*, Elsevier/MK, 2000.
52. D. A. Hodges, *Analysis and Design of Digital Integrated Circuits*, Third Edition, McGraw-Hill, 2003.
53. K. Itoh, *VLSI Memory Chip Design*, Springer, 2001. (Cheap Edition)
54. B. Keeth and R. J. Baker, *DRAM Circuit Design : A Tutorial*, Wiley/IEEE, 2000.
55. K. Martin, *Digital Integrated Circuit Design*, OUP, 1999.
56. B. Prince, *Semiconductor Memories : A Handbook of Design, Manufacture and Application*, Second Edition, Wiley, 1996.
57. D. A. Pucknell and K. Eshraghian, *Basic VLSI Design : Systems and Circuits*, Third Edition, Prentice-Hall, 1994. (Cheap Edition)
58. C. Saint and J. Saint, *IC Layout Basics : A Practical Guide*, McGraw-Hill, 2002.
59. C. Saint and J. Saint, *IC Mask Design : Essential Layout Techniques*, McGraw-Hill, 2002.
60. A. K. Sharma, *Semiconductor Memories : Technology, Testing and Reliability*, Wiley/IEEE, 2002. (Cheap Edition)
61. A. K. Sharma, *Advanced Semiconductor Memories : Architectures, Designs, and Applications*, Wiley/IEEE, 2002. (Cheap Edition ?)

62. I. Sutherland, B. Sproull and D. Harris, *Logical Effort : Designing Fast CMOS Circuits*, Elsevier/MK, 1999.
63. J. P. Uyemura, *Chip Design for Submicron VLSI : CMOS Layout and Simulation*, Thomson, 2005.
64. J. P. Uyemura, *Physical Design of CMOS Integrated Circuits Using L-Edit*, Thomson, 1995.
65. J. P. Uyemura, *Fundamentals of MOS Digital Integrated Circuits*, Addison-Wesley, 1988. (Out of Print)
66. M. M. Vai, *VLSI Design*, CRC Press, 2000.
67. H. J. M. Veendrick, *Deep-Submicron CMOS ICs : From Basics to ASICs*, Kluwer, 2000.
68. N. Weste and K. Eshraghian, *Principles of CMOS VLSI Design : A Systems Perspective*, Revised Second Edition, Pearson/AW, 1999. (Cheap Edition)

ASIC Design, FPGA Design, Reconfigurable Computing

69. V. Betz, J. Rose and A. Marquardt, *Architecture and CAD for Deep-Submicron FPGAs*, Kluwer, 1999.
70. N. G. Einspruch and J. L. Hilbert, *ASIC Technology*, Academic Press, 1991.
71. V. George and J. M. Rabaey, *Low-Energy FPGAs : Architecture and Design*, Kluwer, 2001.
72. M. Gokhale, and P. S. Graham, *Reconfigurable Computing : Accelerating Computation with Field-Programmable Gate Arrays*, Springer, 2005.
73. R. Munden, *ASIC and FPGA Verification : A Guide to Component Modeling*, Elsevier/MK, 2004.
74. Z. Navabi, *Digital Design and Implementation with Field Programmable Devices*, Springer, 2005.
75. F. Nekoogar, *Timing Verification of ASICs*, Prentice-Hall, 2000.
76. F. Nekoogar and F. Nekoogar, *From ASICs to SoCs : A Practical Approach*, Prentice-Hall, 2003.
77. D. Pellerin and S. Thibault, *Practical FPGA Programming in C*, Pearson/PH, 2005.
78. M. A. Richards, A. J. Gadiant and G. A. Frank, *Rapid Prototyping of Application Specific Signal Processors*, Kluwer, 1997.
79. Z. Salcic, *VHDL and FPLDs in Digital System Design, Prototyping and Customization*, Kluwer, 1998.

80. R. C. Seals and G. F. Whapshott, *Programmable Logic : PLDs and FPGAs*, McGraw-Hill, 1997.
81. A. K. Sharma, *Programmable Logic Handbook : PLDs, CPLDs and FPGAs*, McGraw-Hill, 1998.
82. S. M. Trimberger, *FPGA Technology*, Kluwer, 1994.

Analog IC Design, Mixed Signal Design, RF IC Design

83. Analog Devices Inc., *Data Conversion Handbook*, Elsevier/Newnes, 2004.
84. R. J. Baker, H. W. Li and D. E. Boyce, *CMOS : Circuit Design, Layout and Simulation*, IEEE Press, 1998. (Cheap Edition)
85. J. J. Becerra and E. G. Friedman, *Analog Design Issues in Digital VLSI Circuits and Systems*, Kluwer, 1997.
86. E. N. Farag and M. I. Elmasry, *Mixed Signal VLSI Wireless Design : Circuits and Systems*, Springer/Kluwer, 1999.
87. J. E. Franca and Y. Tsvividis, *Design of Analog-Digital VLSI Circuits for Telecommunications and Signal Processing*, Second Edition, Prentice-Hall, 1993.
88. R. Gregorian, *Introduction to CMOS Op-Amps and Comparators*, Wiley, 1999.
89. S. L. Hurst, *VLSI Custom Microelectronics : Digital, Analog and Mixed Signal*, Second Edition, Marcel Dekker, 1998.
90. P. G. A. Jespers, *Integrated Converters : D-A and A-D Architectures, Analysis and Simulation*, OUP, 2001.
91. B. H. Leung, *VLSI for Wireless Communication*, Pearson/PH, 2002.
92. M. Liu, *Demystifying Switched Capacitor Circuits*, Elsevier/Newnes, 2006.
93. R. Ludwig and P. Bretchko, *RF Circuit Design : Theory and Applications*, Prentice-Hall, 2000. (Cheap Edition)
94. E. Sanchez-Sinencio and A. G. Andreou, *Low-Voltage/Low-Power Integrated Circuits and Systems : Low-Voltage Mixed-Signal Circuits*, IEEE Press, 1999.
95. J. R. Smith, *Modern Communications Circuits*, McGraw-Hill, 1997. (Cheap Edition)
96. A. A. Stocker, *Analog VLSI Circuits for the Perception of Visual Motion*, Wiley, 2006.
97. M. Thompson, *Intuitive Analog Circuit Design*, Elsevier/Newnes, 2006.
98. S. Winder, *Analog and Digital Filter Design*, Second Edition, Elsevier/Newnes, 2002.
99. T. Ytterdal, Y. Cheng and T. A. Fjeldly, *Device Modeling for Analog and RF CMOS Circuit Design*, Wiley, 2003.

VLSI Systems Architecture, Computer Architecture, DSP Architecture

100. J. D. Carpinelli, *Computer Systems Organization and Architecture*, Addison-Wesley, 2001.
101. A. Clements, *Principles of Computer Hardware*, Fourth Edition, OUP, 2006.
102. M. D. Ercegovac and T. Lang, *Digital Arithmetic*, Elsevier/MK, 2003.
103. J. Feldman and C. Retter, *A Designer's Text Using a Generic RISC*, McGraw-Hill, 1994.
104. M. J. Flynn and S. F. Oberman, *Advanced Computer Arithmetic Design*, Wiley, 2001.
105. V. C. Hamacher, Z. G. Vranesic, and S. G. Zaky, *Computer Organization*, Fifth Edition, McGraw-Hill, 2002. (Cheap Edition)
106. J. P. Hayes, *Computer Architecture and Organization*, Third Edition, McGraw-Hill, 2002. (Cheap Edition)
107. K. Hwang, *Advanced Computer Architecture : Parallelism, Scalability, Programmability*, McGraw-Hill, 1993. (Cheap Edition)
108. M. Johnson, *Superscalar Microprocessor Design*, Prentice-Hall, 1991.
109. I. Koren, *Computer Arithmetic Algorithms*, Second Edition, AK Peters, 2003. (Cheap Edition)
110. P. Pal Choudhuri, *Computer Organization and Design*, Second Edition, Prentice-Hall, 1999. (Cheap Edition)
111. B. Parhami, *Computer Architecture : From Microprocessors to Supercomputers*, OUP, 2005.
112. B. Parhami, *Computer Arithmetic : Algorithms and Hardware Design*, OUP, 1999.
113. D. A. Patterson and J. L. Hennessy, *Computer Architecture : A Quantitative Approach*, Second Edition, Elsevier/MK, 1996. (Cheap Edition)
114. D. Sima, T. Fountain and P. Kacsuk, *Advanced Computer Architecture : A Design Space Approach*, Addison-Wesley, 1997.
115. H. Stone, *High Performance Computer Architectures*, Second Edition, Addison-Wesley, 1990. (Out of Print)
116. S. Ward and R. Halstead, *Computational Structures*, McGraw-Hill/IEEE Press/MIT Press, 1990.
117. J. P. Shen and M. Lipasti, *Modern Processor Design : Fundamentals of Superscalar Processors*, McGraw-Hill, 2004.
118. V. K. Madisetti, *VLSI Digital Signal Processors*, Butterworth-Heinemann/IEEE Press, 1995. (Out of Print)

119. U. Meyer-Baese, *Digital Signal Processing with Field Programmable Gate Arrays*, Springer, 2001.
120. S. K. Mitra and J. F. Kaiser, *Handbook for Digital Signal Processing*, Wiley, 1993.

VHDL, Verilog and HDL-Based Design

121. H. Bhatnagar, *Advanced ASIC Chip Synthesis Using Synopsys Design Compiler and PrimeTime*, Second Edition, Springer/Kluwer, 2002.
122. S. Brown and Z. Vranesic, *Fundamentals of Digital Logic with VHDL Design*, Second Edition, 2005.
123. B. Cohen, *VHDL Coding Styles and Methodologies*, Second Edition, Kluwer, 1999.
124. B. Cohen, *VHDL Answers to Frequently Asked Questions*, Second Edition, Kluwer, 1998.
125. IEEE Standard 1076-1993, *VHDL Language Reference Manual*, IEEE Press, 1993.
126. Z. Navabi, *VHDL : Analysis and Modeling of Digital Systems*, Second Edition, McGraw-Hill, 1998. (Cheap Edition)
127. D. Naylor and S. Jones, *VHDL : A Logic Synthesis Approach*, Chapman & Hall, 1997.
128. D. E. Ott and T. J. Wilderotter, *A Designer's Guide to VHDL Synthesis*, Kluwer, 1994.
129. V. A. Pedroni, *Circuit Design with VHDL*, MIT Press/PHI, 2004. (Cheap Edition)
130. J. Pick, *VHDL : Techniques, Experiments and Caveats*, McGraw-Hill, 1996. (Cheap Edition)
131. S. Brown and Z. Vranesic, *Fundamentals of Digital Logic with Verilog Design*, 2003.
132. M. D. Ciletti, *Advanced Digital Design With The Verilog HDL*, PHI, 1999. (Cheap Edition)
133. K. Coffman, *Real World FPGA Design with Verilog*, Prentice-Hall, 2000.
134. P. Foote and F. Engelmann, *Verilog HDL Revealed : A Practical Guide to System Simulation*, Prentice-Hall, 2000.
135. IEEE Standard 1394-1995, *Verilog Language Reference Manual*, IEEE Press,
136. W. F. Lee, *Verilog Coding for Logic Synthesis*, Wiley, 2003.
137. Z. Navabi, *Verilog Digital System Design*, Second Edition, McGraw-Hill, 2005.
138. T. R. Padmanabhan and B. B. T. Sundari, *Design Through Verilog HDL*, Wiley, 2004.
139. D. J. Smith, *HDL Chip Design : A Practical Guide*, Doone Publisher, 1997.

VLSI/IC CAD and Algorithms, High-Level Synthesis

140. *Advances in CAD for VLSI*, Volumes 1 – 8, Elsevier/North Holland, 1986-94.
141. P. Banerjee, *Parallel Algorithms for VLSI CAD*, Prentice-Hall, 1994.
142. D. D. Gajski, N. Dutt, A. C-H. Wu and S. Y-L. Lin, *High-Level Synthesis : Introduction to Chip and System Design*, Kluwer, 1992.
143. J. B. Gosling, *Simulation in the Design of Digital Electronic Systems*, CUP, 1993.
144. G. D. Hachtel and F. Somenzi, *Logic Synthesis and Verification Algorithms*, Springer, 2006.
145. S. Hassoun and T. Sasao, *Logic Synthesis and Verification*, Springer, 2001.
146. F. J. Hill and G. R. Peterson, *Computer Aided Logical Design with Emphasis on VLSI*, Fourth Edition, Wiley, 1993.
147. P. Kurup and T. Abbasi, *Logic Synthesis Using Synopsys*, Second Edition, Kluwer, 1996.
148. P. Mazumder and E. Rudnick, *Genetic Algorithms for VLSI Design, Layout and Test Automation*, Pearson/PH/AW, 1999. (Cheap Edition)
149. C. Meinel and T. Theobald, *Algorithms and Data Structures in VLSI Design*, Springer, 1998.
150. P. Michel, U. Lauther and P. Duzy, *The Synthesis Approach to Digital System Design*, Kluwer, 1992.
151. A. Miczo, *Digital Logic Testing and Simulation*, Second Edition, Wiley, 2003.
152. L. T. Pillage, R. A. Rohrer and C. Visweswariah, *Electronic Circuit and System Simulation Methods*, McGraw-Hill, 1998.
153. M. Sarrafzadeh and C. K. Wong, *An Introduction to VLSI Physical Design*, McGraw-Hill, 1996. (Cheap Edition)
154. J. D. Ullman, *Computational Aspects of VLSI*, WH Freeman/CS Press, 1984. (Out of Print)
155. B. Wile, J. Goss and W. Roesner, *Comprehensive Functional Verification : The Complete Industry Cycle*, Elsevier/MK, 2005.

Hardware/Software Codesign, Embedded Systems

156. J-M. Berge, O. Levia and J. Rouillard, *Hardware/Software Co-Design and Co-Verification*, Kluwer, 1996.
157. G. De Micheli and M. Sami (Editors), *Hardware/Software Co-Design*, Kluwer, 1996.

158. J. van den Hurk and J. Jess, *System Level Hardware/Software Codesign : An Industrial Approach*, Kluwer, 1997.
159. P. Eles, K. Kuchcinski and Z. Peng, *System Synthesis with VHDL*, Kluwer, 1998.
160. J. A. Fisher, P. Faraboschi and C. Young, *Embedded Computing : A VLIW Approach to Architecture, Compilers and Tools*, Elsevier/MK, 2004.
161. R. K. Gupta, *Co-synthesis of Hardware/Software for Digital Embedded Systems*, Kluwer, 1995.
162. S. Heath, *Embedded Systems Design*, Second Edition, Elsevier/Newnes, 2002.
163. P. Ienne and R. Leupers, *Customizable Embedded Processors : Design Technologies and Applications*, Elsevier/MK, 2006. (To Appear)
164. S. Iman and M. Pedram, *Logic Synthesis for Low-Power VLSI Designs*, Kluwer, 1997.
165. A. Jerraya and W. Wolf, *Multiprocessor Systems-on-Chips*, Elsevier/MK, 2004.
166. J. C. Lopez, R. Hermida and W. Gesselhardt, *Advance Techniques for Embedded Systems Design and Test*, Kluwer, 1998.
167. D. E. Ott and T. J. Wilderotter, *A Designer's Guide to VHDL Synthesis*, Kluwer, 1994.
168. *Proceeding of CODES Conference*, IEEE Press. (Available for Various Years)

VLSI Testing, Testability and Formal Verification

169. V. Agrawal and S. C. Seth, *Test Generation for VLSI Chips*, IEEE CS Press, 1989.
170. A. Crouch, *Design for Test for Digital ICs and Embedded Core Systems*, Prentice-Hall, 2000.
171. T. Fitzpatrick, A. Salz, D. Rich and S. Sutherland, *SystemVerilog for Verification*, Springer, 2006.
172. S. L. Hurst, *VLSI Testing : Digital and Mixed Analog/Digital Techniques*, INSPEC/IEE, 1999.
173. P. K. Lala, *Self-Checking and Fault-Tolerant Digital Design*, Elsevier/MK, 2000.
174. W. K. Lam, *Hardware Design Verification : Simulation and Formal Method-Based Approaches*, Prentice-Hall, 2005.
175. C. Maunder, *The Board Designer's Guide to Testable Logic Circuits*, Addison-Wesley, 1992.
176. S. Mourad and Y. Zorian, *Principles of Testing Electronic Systems*, Wiley, 2000.
177. K. P. Parker, *The Boundary Scan Handbook : Analog and Digital*, Third Edition, Springer/Kluwer, 2003.

178. D. L. Perry and H. Foster, *Applied Formal Verification : For Digital Circuit Design*, McGraw-Hill, 2005.
179. D. K. Pradhan, *Fault-Tolerant Computing : Theory and Techniques*, Volume I, Prentice-Hall, 1986. (Out of Print)
180. D. K. Pradhan, *Fault-Tolerant Computing : Theory and Techniques*, Volume II, Prentice-Hall, 1986. (Out of Print)
181. R. Rajsuman, *System-on-a-Chip Design and Test*, Artech, 2000.
182. B. Vinnakota, *Analog and Mixed-Signal Test*, Prentice-Hall, 1998.
183. B. Wile, J. Goss and W. Roesner, *Comprehensive Functional Verification : The Complete Industry Cycle*, Elsevier/MK, 2005.

Low-Power Design Techniques

184. A. Bellaouar and M. I. Elmasry, *Low-Power Digital VLSI Design : Circuits and Systems*, Kluwer, 1995.
185. A. P. Chandrakasan and R. W. Broderson, *Low Power Digital CMOS Design*, Kluwer, 1995.
186. V. Kursun and E. G. Friedman, *Multi-voltage CMOS Circuit Design*, Wiley, 2006.
187. J. M. Rabaey and M. Pedram, *Low Power Design Methodologies*, Kluwer, 1996.
188. G. K. Yeap, *Practical Low-Power Digital VLSI Design*, Kluwer, 1997.

VLSI Interconnects and Analysis

189. C. A. Harper, *Electronic Packaging and Interconnection Handbook*, McGraw-Hill, 2004.
190. A. B. Kahng and G. Robins, *On Optimal Interconnects for VLSI*, Kluwer, 1994.
191. F. Moll and M. Roca, *Interconnection Noise in VLSI Circuits*, Springer, 2004.

System Design, System Architecture

192. D. Anderson, *Universal Serial Bus (USB) System Architecture*, Addison-Wesley, 1997.
193. R. Budruk, D. Anderson, T. Shanley, *PCI Express System Architecture*, Addison-Wesley, 2004.
194. F. M. Cady, *Microcontrollers and Microcomputers : Principles of Software and Hardware Engineering*, OUP, 1997.

195. R. Ford and C. Coulston, *Design for Electrical and Computer Engineers : Theory Concepts and Practice*, McGraw-Hill, 2005.
196. R. J. Hannemann, A. D. Kraus and M. Pecht, *Semiconductor Packaging : A Multidisciplinary Approach*, Wiley, 1997.
197. K. J. Hintz and D. Tabak, *Microcontrollers : Architecture, Implementation and Programming*, McGraw-Hill, 1992.
198. F. T. Leighton, *Introduction to Parallel Algorithms and Architectures : Arrays, Trees, Hypercubes*, Elsevier/MK, 1992.
199. B. M. Lunt, *Electronic Physical Design*, Prentice-Hall, 2003.
200. S. McDowell and M. D. Seyer, *USB Explained*, Prentice-Hall, 1999.
201. D. K. Pradhan, *Fault-Tolerant Computer System Design*, Prentice-Hall, 1996.
202. T. Shanley and D. Anderson, *PCI System Architecture*, Third Edition, Addison-Wesley, 1995.
203. N. R. Storey, *Electronics : A Systems Approach*, Second Edition, Addison-Wesley, 1998. (Cheap Edition)
204. R. K. Ulrich and W. D. Brown, *Advanced Electronic Packaging*, Second Edition, Wiley/IEEE, 2006.

Digital Logic Design

205. M. Balch, *Complete Digital Design : A Comprehensive Guide to Digital Electronics and Computer System Architecture*, McGraw-Hill, 2003.
206. D. J. Comer, *Digital Logic and State Machine Design*, OUP, 1994.
207. T. A. DeMassa and Z. Ciccone, *Digital Integrated Circuits*, Wiley, 1995.
208. M. French, *Conceptual Design for Engineers*, Third Edition, Springer, 1998.
209. J. P. Hayes, *Introduction to Digital Logic Design*, Addison-Wesley, 1993.
210. B. Holdsworth and C. Woods, *Digital Logic Design*, Fourth Edition, Elsevier/Newnes, 2002.
211. Z. Kohavi, *Switching and Finite Automata Theory*, Tata McGraw-Hill, 1978. (Cheap Edition)
212. Z. Kohavi, R. W. Hamming and E. A. Feigenbaum, *Switching and Finite Automata Theory*, Second Edition, McGraw-Hill, 1986.
213. S. Lee, *Design of Computers and Other Complex Digital Devices*, Prentice-Hall, 2000.
214. M. Mano, *Digital Design*, Second Edition, PHI, 1991. (Cheap Edition)

215. M. Mano, *Digital Logic and Computer Design*, PHI, 199x. (Cheap Edition)
216. M. Mano and C. Kime, *Logic and Computer Design Fundamentals*, Second Edition, Prentice-Hall, 2000.
217. Z. Navabi, *Digital Design and Implementation with Field Programmable Devices*, Springer, 2005.
218. S. G. Shiva, *Introduction to Logic Design*, Second Edition, Marcel Dekker, 1998.
219. J. P. Uyemura, *Digital Systems Design : An Integrated Approach*, Thomson, 2000.
220. N. Wirth, *Digital Circuit Design*, Springer, 1995.

Linux/Unix System Administration

1. P. Albitz and C. Liu, *DNS and BIND*, Fifth Edition, OSA, 2006. (Cheap Edition)
2. D. J. Barrett, R. E. Silverman and R. G. Byrnes, *SSH, The Secure Shell : The Definitive Guide*, Second Edition, OSA, 2005.
3. T. Batts, T. Dawson and G. N. Purdy, *Linux Network Administrator's Guide*, Third Edition, OSA, 2005.
4. M. Bishop, *Computer Security : Art and Science*, Addison-Wesley, 2003.
5. D. N. Blank-Edelman, *Perl for System Administration*, OSA, 2000.
6. G. Carter, *LDAP System Administration*, OSA, 2003.
7. T. Collings and K. Wall, *Red Hat Linux Networking and System Administration*, Third Edition, Wiley, 2005.
8. B. Costales and Eric Allman, *Sendmail*, Third Edition, OSA, 2002. (Cheap Edition)
9. B. Costales, G. Jansen, C. Assmann and G. Shapiro, *Sendmail 8.13 Companion*, OSA, 2004.
10. M. K. Dalheimer and M. Welsh, *Running Linux*, Fifth Edition, OSA, 2005.
11. M. Jang, *RHCE Linux Study Guide*, Fourth Edition, McGraw-Hill, 2004.
12. B. Laurie and P. Laurie, *Apache : The Definitive Guide*, Third Edition, OSA, 2002.
13. R. Lehtinen, *Computer Security Basics*, Second Edition, OSA, 2006.
14. I. Ristic, *Apache Security*, OSA, 2005.
15. J. N. Robbins, *Web Design in a Nutshell*, Third Edition, OSA, 2006.
16. R. L. Schwartz, T. Phoenix and B. D. Foy, *Learning Perl*, Fourth Edition, OSA, 2005. (Cheap Edition)

17. E. Siever, A. Weber, S. Figgins, R. Love and A. Robbins, *Linux in a Nutshell*, Fifth Edition, O'Reilly, 2005. (Cheap Edition)
18. M. G. Sobell, *A Practical Guide to Red Hat Linux*, Second Edition, Pearson/PH, 2004. (Cheap Edition)
19. T. Steidler-Dennison, *Run Your Own Web Server Using Linux and Apache*, SitePoint/O'Reilly, 2005.
20. W. R. Stevens and S. A. Rago, *Advanced Programming in the Unix Environment*, Second Edition, Pearson/AW, 2005.

VLSI Fabrication Technology

1. A. Amerasekera and F. N. Najm, *Failure Mechanisms in Semiconductor Devices*, Second Edition, Wiley, 1997.
2. K. J. Bachmann, *The Materials Science of Microelectronics*, Wiley, 1994.
3. C. Y. Chang and S. M. Sze, *ULSI Technology*, McGraw-Hill, 1996.
4. A. Christou, *Electromigration and Electronic Device Degradation*, Wiley, 1993.
5. M. J. Madou, *Fundamentals of Microfabrication*, Second Edition, CRC, 2002.
6. P. R. Shepherd, *IC : Design, Fabrication and Test*, McGraw-Hill, 1996.
7. R. R. Troutman, *Latchup in CMOS Technology : The Problem and Its Cure*, Kluwer, 1989.
8. C-H. Tung, G. T. T. Sheng, C-Y. Lu, *ULSI Semiconductor Technology Atlas*, Wiley, 2003.

Semiconductor Devices, Device Modeling and Simulation, SPICE

9. K. F. Brennan, *The Physics of Semiconductors with Applications to Optoelectronic Devices*, CUP, 1999.
10. G. F. Carey, W. B. Richardson, C. S. Reed and B. Mulvaney, *Circuit, Device and Process Simulation : Mathematical and Numerical Aspects*, Wiley, 1996.
11. J. A. Connelly and P. Choi, *Macromodeling with SPICE*, Prentice-Hall, 1992.
12. D. K. Ferry and R. O. Grondin, *Physics of Submicron Devices*, Plenum, 1991.
13. G. Di Giacomo, *Reliability of Electronic Packages and Semiconductor Devices*, McGraw-Hill, 1996.
14. C. Hamaguchi, *Basic Semiconductor Physics*, Springer, 2006.
15. K. Hess, *Advanced Theory of Semiconductor Devices*, Wiley/IEEE, 1999. (Cheap Edition)
16. K. Hess, *Computational Electronics : Semiconductor Transport and Device Simulation*, Kluwer, 1991.
17. S. O. Kasap, *Principles of Electronic Materials and Devices*, Third Edition, McGraw-Hill, 2005.
18. K. M. Kramer and W. N. G. Hitchon, *Semiconductor Devices : A Simulation Approach*, Prentice-Hall, 1997.
19. K. S. Kundert, *The Designer's Guide to SPICE and SPECTRE*, Kluwer, 1995.

20. J. J. Liou, A. Ortiz-Conde and F. Gracia-Sanchez, *Analysis and Design of MOSFETs : Modeling, Simulation and Parameter Extraction*, Kluwer, 1998.
21. D. A. Neamen, *Semiconductor Physics and Devices : Basic Principles*, Second Edition, Wiley, 2002.
22. R. J. Pierrett, *Semiconductor Device Fundamentals*, Prentice-Hall, 1996.
23. R. J. Pierrett, *Advanced Semiconductor Fundamentals*, Second Edition, Pearson/PH, 2002.
24. C-T. Sah, *Fundamentals of Solid State Electronics*, World Scientific, 1991.
25. A. Schenk, *Advanced Physical Models for Silicon Device Simulation*, Springer, 1998.
26. J. Singh, *Semiconductor Devices : Basic Principles*, Wiley, 2000.
27. J. Singh, *Semiconductor Devices : An Introduction*, McGraw-Hill, 1994.
28. M. S. Tyagi, *Introduction to Semiconductor Materials and Devices*, Wiley, 1991.
29. S. H. Voldman, *ESD : Physics and Devices*, Wiley, 2004.
30. W. T. Wenckebach, *Essentials of Semiconductor Physics*, Wiley, 1999.

VLSI Design, VLSI Subsystem Design

31. E. A. Amerasekera and C. Duvvury, *ESD in Silicon Integrated Circuits*, Second Edition, Wiley, 2002.
32. S. Dabral and T. Maloney, *Basic ESD and I/O Design*, Wiley, 1998.
33. K. Gopalan, *Introduction to Digital Microelectronics Circuits*, Irwin, 1996.
34. C. J. Myers, *Asynchronous Circuit Design*, Wiley, 2001.
35. V. G. Oklobdzija, V. M. Stojanovic, D. M. Markovic and N. M. Nedovic, *Digital System Clocking : High-Performance and Low-Power Aspects*, Wiley/IEEE, 2003.
36. B. Prince, *High Performance Memories : New Architectures DRAMs and SRAMs*, Revised Edition, Wiley, 1999.
37. B. Razavi, *Design of Integrated Circuits for Optical Communications*, McGraw-Hill, 2003.
38. R. Seepold and A. Kunzmann, *Reuse Techniques for VLSI Design*, Kluwer, 1999.
39. S. H. Voldman, *ESD : Circuits and Devices*, Wiley, 2006.

ASIC Design, FPGA Design, Reconfigurable Computing

40. S. D. Brown, R. J. Francis, J. Rose and Z. G. Vranesic, *Field Programmable Gate Arrays*, Kluwer, 1992.
41. R. C. Cofer and B. F. Harding, *Rapid System Prototyping with FPGAs : Accelerating the Design Process*, Elsevier/Newnes, 2005.
42. J. Di Giacomo, *Designing with High Performance ASICs*, Prentice-Hall, 1992.
43. C. Maxfield, *The Design Warrior's Guide to FPGAs*, Elsevier/Newnes, 2004. (Cheap Edition)
44. R. Murgai, R. K. Brayton and A. Sangiovanni-Vincentelli, *Logic Synthesis for FPGAs*, Kluwer, 1995.
45. M. S. B. Romdhane, V. K. Madiseti and J. W. Hines, *Quick-Turnaround ASIC Design in VHDL Core-Based Behavioral Synthesis*, Kluwer, 1996.
46. B. Zeidman, *Designing with FPGAs and CPLDs*, CMP Books, 2002.

Analog IC Design, Mixed Signal Design, RF IC Design

47. R. E. Best, *Phase-locked Loops : Theory, Design and Applications*, Fifth Edition, McGraw-Hill, 2003.
48. G. Bianchi, *Phase-Locked Loop Synthesizer Simulation*, McGraw-Hill, 2005.
49. P. V. Brennan, *Phase-locked Loops : Principles and Practices*, McGraw-Hill, 1996.
50. D. J. Comer and D. T. Comer, *Fundamentals of Electronic Circuit Design*, Wiley, 2003.
51. D. J. Comer and D. T. Comer, *Advanced Electronic Circuit Design*, Wiley, 2003.
52. N. Dye and H. Granberg, *Radio Frequency Transistors : Principles and Practical Applications*, Second Edition, Elsevier/Newnes, 2001.
53. J. Everard, *Fundamentals of RF Circuit Design with Low Noise Oscillators*, Wiley, 2001.
54. S. Franco, *Design with Operational Amplifiers and Analog Integrated Circuits*, Third Edition, McGraw-Hill, 2002.
55. R. L. Geiger, P. E. Allen and N. R. Strader, *VLSI Design Techniques for Analog and Digital Circuits*, McGraw-Hill, 1990. (Out of Print)
56. R. Goyal, *High Frequency Analog IC Design*, Wiley, 1995.
57. P. Gray, *Analog MOS Integrated Circuits*, IEEE Press, 1980. (Out of Print)
58. P. Gray, *Analog MOS Integrated Circuits II*, IEEE Press, 1990. (Out of Print)

59. A. B. Grebene, *Bipolar and MOS Analog Integrated Circuit Design*, Wiley, 1984/2002.
60. M. Ismail and T. Fiez, *Analog VLSI : Signal and Information Processing*, McGraw-Hill, 1994. (Out of Print)
61. C. Mead, *Analog VLSI and Neural Systems*, Addison-Wesley, 1989. (Out of Print)
62. P. V. A. Mohan, V. Ramachandran and M. N. S. Swamy, *Switched Capacitor Filters : Theory, Analysis and Design*, Prentice-Hall, 1995.
63. E. S. Ochotta, T. Mukherjee, R. A. Rutenbar and L. R. Carley, *Practical Synthesis of High-Performance Analog Circuits*, Kluwer, 1998.
64. S. R. Norsworthy, R. Schreier and G. C. Temes, *Delta-Sigma Data Converters : Theory, Design, and Simulation*, Wiley/IEEE, 1996.
65. V. Peluso, M. Steyaert, W. M. C. Sansen, *Design of Low-Voltage Low-Power CMOS Delta-Sigma A/D Converters*, Kluwer, 1999.
66. S. Rabbii and B. A. Wooley, *Design of Low-Voltage Low-Power Sigma-Delta Modulators*, Kluwer, 1998.
67. M. H. Rashid, *Microelectronic Circuits : Analysis and Design*, Thomson, 1999.
68. B. Razavi, *RF Microelectronics*, IEEE Press, 19xx.
69. B. Razavi, *Monolithic Phase-locked Loops and Clock Recovery Circuits : Theory and Design*, IEEE Press, 1996.
70. B. Razavi, *Phase-locking in High-Performance Systems : From Devices to Architectures*, Wiley/IEEE Press, 2003.
71. R. A. Rutenbar, G. G. E. Gielen and B. A. Antao, *Computer-Aided Design of Analog Integrated Circuits and Systems*, Wiley/IEEE, 2002.
72. J. Silva-Martinez, M. Steyaert, and W. Sansen, *High-Performance CMOS Continuous-Time Filters*, Kluwer, 1993.
73. C. Tomazou, J. B. Hughes, N. C. Buttersby, *Switched Current : An Analog Technique for Digital Techniques*, IEE Press, 1993.
74. R. Unbehauen and A. Cichocki, *MOS Switched Capacitor and Continuous-Time ICs and Systems*, Springer, 1989.
75. P. Wambneq and W. M. C. Sansen, *Distortion Analysis of Analog Integrated Circuits*, Kluwer, 1998.
76. C. J. Weisman, *The Essential Guide to RF and Wireless*, Second Edition, Pearson/PH, 2002.
77. D. H. Wolaver, *Phase-locked Loop Circuit Design*, Prentice-Hall, 1991.

VLSI Systems Architecture, Computer Architecture, DSP Architecture

78. D. E. Comer, *Essentials of Computer Architecture*, Pearson/PH, 2004.
79. A. L. DeCegama, *Parallel Processing Architectures and VLSI Hardware*, Volume 1, Prentice-Hall, 1989.
80. U. Golze, *VLSI Chip Design with the HDL Verilog*, Springer, 1996.
81. G. Kane and J. Heinrich, *MIPS RISC Architecture*, Prentice-Hall, 1992.
82. M. Lu, *Arithmetic and Logic in Computer Systems*, Wiley, 2004.
83. M. Malone, *The Microprocessor : A Biography*, Springer, 1995.
84. M. Murdocca, *Computer Organization and Architecture*, Wiley, 2006. (To Appear)
85. M. J. Murdocca and V. P. Heuring, *Principles of Computer Architecture*, Prentice-Hall, 2000.
86. N. Nisan, S. Schocken, *The Elements of Computing Systems : Building a Modern Computer from First Principles*, MIT Press/PHI, 200x. (Cheap Edition)
87. P. V. S. Rao, *Perspectives in Computer Architecture*, PHI, 200x. (Cheap Edition)
88. P. M. Sailer and D. R. Kaeli, *The DLX Instruction Set Architecture Handbook*, Elsevier/MK, 1996.
89. SPARC International, *SPARC Architecture Manual*, Prentice-Hall, 1992.
90. E. E. Swartzlander, *Computer Arithmetic*, Volume I, IEEE Press, 1990. (Out of Print)
91. E. E. Swartzlander, *Computer Arithmetic*, Volume II, IEEE Press, 1990. (Out of Print)
92. C. Thimmannagari, *CPU Design : Answers to Frequently Asked Questions*, Springer, 2005.
93. D. L. Weaver and T. Germond, *The SPARC Architecture Manual (Version 9)*, Prentice-Hall, 1994.

VHDL, Verilog and HDL-Based Design

94. L. Baker, *VHDL Programming with Advanced Topics*, Wiley, 1993.
95. J. Bhasker, *A Guide to VHDL Syntax*, Prentice-Hall, 1995.
96. D. R. Coelho, *The VHDL Handbook*, Kluwer, 1989. (Out of Print)
97. A. Dewey, *Analysis and Design of Digital Systems with VHDL*, Thomson, 1997.
98. R. Lipsett, C. Schaefer and C. Ussery, *VHDL : Hardware Description and Design*, Kluwer, 1989. (Out of Print)

99. S. Lee, *Advanced Digital Logic Design Using VHDL, State Machines, and Synthesis for FPGAs*, Thomson, 2006.
100. D. L. Perry, *VHDL : Programming by Example*, Fourth Edition, McGraw-Hill, 2002. (Cheap Edition)
101. C. H. Roth, *Digital System Design with VHDL*, Thomson, 1998. (Cheap Edition)
102. M. Zwolinski, *Digital System Design with VHDL*, Second Edition, Prentice-Hall, 2003.
103. N. M. Botros, *HDL Programming Fundamentals : VHDL and Verilog*, Thomson/Delmar, 2006.
104. J. M. Lee, *Verilog Quickstart*, Kluwer, 1997.
105. S. Lee, *Advanced Digital Logic Design Using Verilog, State Machines, and Synthesis for FPGAs*, Thomson, 2006.
106. E. Mednick, *Elements of Verilog Style*, Prentice-Hall, 1999.
107. V. Sagdeo, *The Complete Verilog Book*, Kluwer, 1998.

VLSI/IC CAD and Algorithms, High-Level Synthesis

108. E. Aarts and J. K. Lenstra, *Local Search in Combinatorial Optimization*, Wiley, 1997.
109. R. Camposano and W. Wolf, *High-level VLSI Synthesis*, Kluwer, 1991.
110. S. Devadas, A. Ghosh and K. Keutzer, *Logic Synthesis*, McGraw-Hill, 1994.
111. A. Kuehlmann, *The Best of ICCAD : 20 Years of Excellence in Computer-Aided Design*, Springer, 2003.
112. E. Lawler, *Combinatorial Optimization : Networks and Matroids*, Dover, 2001.
113. R. Motwani and P. Raghavan, *Randomized Algorithms*, CUP, 1995.
114. C. H. Papadimitriou and K. Steiglitz, *Combinatorial Optimization : Algorithms and Complexity*, PHI, 1982. (Cheap Edition)
115. R. Raghuram, *Computer Simulation of Electronic Circuits*, Wiley, 1989. (Cheap Edition)
116. S. Rubin, *Computer Aids for VLSI Design*, Addison-Wesley, 1987. (Out of Print) See <http://www.rulabinsky.com/cavd/> for Online Second Edition
117. T. Sasao, *Switching Theory for Logic Synthesis*, Kluwer, 1999.
118. S. M. Trimberger, *An Introduction to CAD for VLSI*, Kluwer, 1987. (Out of Print)
119. V. V. Vazirani, *Approximation Algorithms*, Springer, 2004.

120. R. A. Walker and R. Camposano, *A Survey of High-level Synthesis Systems*, Kluwer, 1991.

Hardware/Software Codesign, Embedded Systems

121. S. Ball, *Embedded Microprocessor Systems : Real World Design*, Third Edition, Elsevier/Newnes, 2003.
122. A. S. Berger, *Embedded Systems Design : An Introduction to Processes, Tools and Techniques*, CMP Books, 2001.
123. C. Hollabaugh, *Embedded Linux : Hardware, Software, and Interfacing*, Addison-Wesley, 2002.
124. Q. Li and C. Yao, *Real-Time Concepts for Embedded Systems*, CMP Books, 2003.
125. R. Niemann and P. Marwedel, *Hardware/Software Co-Design for Data Flow Dominated Embedded Systems*, Kluwer, 1998.
126. P. Raghavan, A. Lad and S. Neelakandan, *Embedded Linux System Design and Development*, Auerbach, 2005.
127. K. Yagmour, *Building Embedded Linux Systems*, ORA, 2003.
128. T-Y. Yen and W. Wolf, *Hardware/Software Co-Synthesis of Distributed Embedded Systems*, Kluwer, 1996.

VLSI Testing, Testability and Formal Verification

129. P. H. Bardell, W. H. McAnney and J. Savir, *Built-in Self Test for VLSI : Pseudorandom Techniques*, Wiley, 1987.
130. H. Bleeker, P. van den Eijnden and F. de Jong, *Boundary-Scan Test : A Practical Approach*, Kluwer, 1993.
131. R. J. Feugate and S. M. McIntyre, *Introduction to VLSI Testing*, Prentice-Hall, 1988.
132. M. Nicolaidis, Y. Zorian and D. K. Pradhan, *Online Testing for VLSI*, Kluwer, 1998.
133. V. N. Yarmolik, *Fault Diagnosis of Digital Circuits*, Wiley, 1990.

Low-Power Design Techniques

134. S. S. Rofail and K-S. Yeo, *Low-Voltage Low-Power Digital BiCMOS Circuits : Circuit Design, Comparative Study and Sensitivity*, Prentice-Hall, 1999.

VLSI Interconnects and Analysis

135. C. A. Harper, *Electronic Packaging and Interconnection Handbook*, Second Edition, McGraw-Hill, 1997.
136. C. A. Harper and M. B. Miller, *Electronic Packaging, Microelectronics and Interconnection Dictionary*, McGraw-Hill, 1993.

System Design, System Architecture

137. C. F. Coombs, *Coomb's Printed Circuits Handbook*, Fifth Edition, McGraw-Hill, 2001.
138. I. Englander, *The Architecture of Computer Hardware and Systems Software : An Information Technology Approach*, Third Edition, Wiley, 2003. (Cheap Edition)
139. T. Granberg, *Handbook of Digital Techniques for High-Speed Design*, Prentice-Hall, 2004.
140. A. Jerraya and W. Wolf, *Multiprocessor Systems-on-Chips*, Elsevier/MK, 2004.
141. R. S. Khandpur, *Printed Circuit Boards*, McGraw-Hill, 2005.
142. S. G. Konsowski and A. R. Helland, *Electronic Packaging of High Speed Circuitry*, McGraw-Hill, 1997.
143. J. H. Lau, W. Nakayama, J. Prince and C. P. Wong, *Electronic Packaging : Design, Materials, Process and Reliability*, McGraw-Hill, 1998.
144. H. W. Ott, *Noise Reduction Techniques in Electronic Systems*, Second Edition, Wiley, 1988.
145. E. Rehtin, *Systems Architecting : Creating and Building Complex Systems*, Prentice-Hall, 1991.
146. J. E. Salt and R. Rothery, *Design for Electrical and Computer Engineers*, Wiley, 2002.
147. W. C. Wray, J. D. Greenfield and R. Bannatyne, *Using Microprocessors and Microcomputers : The Motorola Family*, Fourth Edition, Prentice-Hall, 1999.

Digital Logic Design

148. W. Fletcher, *An Engineering Approach to Digital Design*, PHI, 199x. (Cheap Edition)
149. E. O. Hwang, *Digital Logic and Microprocessor Design with VHDL*, Thomson, 2006.
150. R. H. Katz and G. Borriello, *Contemporary Logic Design*, Second Edition, Pearson/BC, 2004.

151. J. E. Palmer and D. Perlman, *Introduction to Digital Systems*, Schaum Series, McGraw-Hill, 1992. (Cheap Edition)
152. C. H. Roth, *Fundamentals of Logic Design*, Fifth Edition, Thomson, 2004.
153. J. M. Yarbrough, *Digital Logic : Applications and Design*, Thomson, 1997.

Linux/Unix System Administration

1. J. Brittain and I. F. Darwin, *Tomcat : The Definitive Guide*, ORA, 2003.
2. M. Burgess, *Principles of Network and System Administration*, Wiley, 2000.
3. T. Christiansen and N. Torkington, *Perl Cookbook*, Second Edition, ORA, 2003.
4. C. Henderson, *Building Scalable Web Sites*, ORA, 2006.
5. E. Meyer, *Cascading Style Sheets : The Definitive Guide*, Second Edition, ORA, 2004.
6. C. Peikari and A. Chuvakin, *Security Warrior*, ORA, 2004.
7. S. Shah and W. Soyinka, *Linux Administration : A Beginner's Guide*, Fourth Edition, McGraw-Hill, 2005.
8. S. Warden, D. Conway and C. Poe, *Perl Hacks*, ORA, 2006.
9. L. Welling and L. Thomson, *PHP and MySQL Web Development*, Third Edition, Sams, 2004.
10. H. E. Williams and D. Lane, *Web Database Applications with PHP and MySQL*, Second Edition, ORA, 2004.
11. E. D. Zwicky, S. Cooper and D. B. Chapman, *Building Internet Firewalls*, Second Edition, ORA, 2000. (Cheap Edition)

Special Topics

1. D. G. Alciatore and M. B. Histan, *Introduction to Mechatronics and Measurement Systems*, Third Edition, McGraw-Hill, 2007. (To Appear)
2. P. Alexander, *System Level Design with Rosetta*, Elsevier/MK, 2006. (To Appear)
3. P. Ashenden, G. Peterson and D. Teegarden, *The System Designer's Guide to VHDL-AMS : Analog, Mixed-Signal, and Mixed-Technology Modeling*, Elsevier/MK, 2002.
4. S. S. Bhattacharyya, *Domain-Specific Processors : Systems, Architectures, Modeling, and Simulation*, CRC Press, 2003.
5. J. Billingsley, *Essentials of Mechatronics*, Wiley, 2006.
6. S. Cetinkunt, *Mechatronics*, Wiley, 2006.
7. W-K. Chen, *The VLSI Handbook*, CRC Press, 2000.

8. A. Dhawan, *Medical Image Analysis*, Wiley, 2003.
9. K. E. Drexler, *Nanosystems : Molecular Machinery, Manufacturing, and Computation*, Wiley, 1992.
10. D. Drusinsky, *Modeling and Verification Using UML Statecharts*, Elsevier/Newnes, 2006.
11. K. Gilleo, *MEMS/MOEM Packaging*, McGraw-Hill, 2006.
12. J. James, M. Halls and J. de Mello, *Molecular Semiconductors : An Introduction*, Wiley, 2006.
13. H. Klauk, *Organic Electronics : Materials, Manufacturing and Applications*, Wiley, 2006.
14. C. Liu, *Foundations of MEMS*, Pearson/PH, 2006.
15. S. Mann, *Intelligent Image Processing*, Wiley/IEEE, 2001.
16. K. McEvoy and J. V. Tucker, *Theoretical Foundations of VLSI Design*, CUP, 2003.
17. K. Mullen and U. Scherf, *Organic Light Emitting Devices : Synthesis, Properties and Applications*, Wiley, 2006.
18. G. Pelz, *Mechatronic Systems : Modelling and Simulation with HDLs*, Wiley, 2003.
19. G. M. Rebeiz, *RF MEMS : Theory, Design, and Technology*, Wiley, 2003.
20. D. Shetty and R. Kolk, *Mechatronics System Design*, Thomson, 1998.
21. M. W. Spong, S. Hutchinson and M. Vidyasagar, *Robot Modeling and Control*, Wiley, 2006.
22. V. Varadan, K. J. Vinoy and S. Gopalakrishnan, *Smart Material Systems and MEMS : Design and Development Methodologies*, Wiley, 2006.
23. A. Vassighi and M. Sachdev, *Thermal and Power Management of Integrated Circuits*, Springer, 2006.
24. A. G. Webb, *Introduction to Biomedical Imaging*, Wiley, 2003.
25. W. Whyte, *Cleanroom Design*, Second Edition, Wiley, 1999.
26. W. Whyte, *Cleanroom Technology : Fundamentals of Design, Testing and Operation*, Wiley, 2001.
27. O. Yadid-Pecht and R. Etienne-Cummings, *CMOS Imagers : From Phototransduction to Image Processing*, Springer, 2004.

Other DSP Related Books

1. A. Ambardar, *Digital Signal Processing : A Modern Introduction*, Thomson, 2006.

2. T. Bose, *Digital Signal and Image Processing*, Wiley, 2004.
3. R. N. Bracewell, *The Fourier Transform and Its Applications*, Third Edition, McGraw-Hill, 2000.
4. C. S. Burrus, R. A. Gopinath and H. Guo, *Introduction to Wavelets and Wavelet Transforms : A Primer*, Prentice-Hall, 1998.
5. D. G. Childers, *Speech Processing and Synthesis Toolboxes*, Wiley, 2000.
6. D. Danieli and Paul Embree, *C++ Algorithms for Digital Signal Processing*, Second Edition, Prentice-Hall, 1999. (Cheap Edition ?)
7. O. K. Ersoy, *Fourier Related Transforms, Fast Algorithms and Applications*, Prentice-Hall, 1997.
8. B. Gold and N. Morgan, *Speech and Audio Signal Processing : Processing and Perception of Speech and Music*, Wiley, 2000.
9. J. C. Goswami and A. K. Chan, *Fundamentals of Wavelets : Theory, Algorithms, and Applications*, Wiley, 1999.
10. E. Ifeachor and B. Jervis, *Digital Signal Processing : A Practical Approach*, Second Edition, Pearson/PH, 2002.
11. E. Lai, *Practical Digital Signal Processing*, Elsevier/Newnes, 2003.
12. R. G. Lyons, *Understanding Digital Signal Processing*, Second Edition, Pearson/PH, 2004.
13. V. K. Madiseti and D. Williams, *The Digital Signal Processing Handbook*, CRC Press, 1997.
14. S. K. Mitra, *Digital Signal Processing : A Computer Based Approach*, Third Edition, McGraw-Hill, 2006. (Cheap Edition ?)
15. A. V. Oppenheim, R. W. Schaffer and J. R. Buck, *Discrete-Time Signal Processing*, Second Edition, Prentice-Hall, 1999. (Cheap Edition)
16. K. K. Parhi and T. Nishitani, *Digital Signal Processing for Multimedia Systems*, Marcel-Dekker, 1999.
17. S. Smith, *Digital Signal Processing : A Practical Guide for Engineers and Scientists*, Elsevier/Newnes, 2002.
18. D. Stranneby and W. Walker, *Digital Signal Processing and Applications*, Second Edition, Elsevier/Newnes, 2004.
19. J. G. Proakis and D. K. Manolakis, *Digital Signal Processing*, Fourth Edition, Pearson/PH, 2006. (Cheap Edition)
20. T. F. Quatieri, *Discrete-Time Speech Signal Processing : Principles and Practice*, Prentice-Hall, 2002.

Computer Science Related Books

1. T. Acharya and A. K. Ray, *Image Processing : Principles and Applications*, Wiley, 2005.
2. A. V. Aho and J. D. Ullman, *Foundations of Computer Science*, C Edition, WH Freeman/CS Press, 1995.
3. A. V. Aho, R. Sethi and J. D. Ullman, *Compilers : Principles, Techniques and Tools*, Second Edition, Pearson/AW, 1986. (Cheap Edition)
4. E. Alpaydin, *Introduction to Machine Learning*, MIT Press, 2004.
5. E. Angel, *Interactive Computer Graphics : A Top-Down Approach Using OpenGL*, Fourth Edition, Pearson/AW, 2006.
6. J. G. Brookshear, *Computer Science : An Overview*, Ninth Edition, Pearson/AW, 2006.
7. T. H. Cormen, C. E. Leiserson, R. L. Rivest and C. Stein, *Introduction to Algorithms*, Second Edition, MIT Press, 2001. (Cheap Edition)
8. M. de Berg, M. van Kreveld, M. Overmars and O. Schwarzkopf, *Computational Geometry*, Second Edition, Springer, 2000.
9. J. A. Dossey, A. D. Otto, L. E. Spence and C. V. Eynden, *Discrete Mathematics*, Fifth Edition, Pearson/AW, 2006.
10. J. D. Foley, A. van Dam, S. K. Feiner and J. F. Hughes, *Computer Graphics : Principles and Practice in C*, Second Edition, Pearson/AW, 1996. (Cheap Edition)
11. R. C. Gonzalez and R. E. Woods, *Digital Image Processing*, Second Edition, Pearson/PH, 2002. (Cheap Edition)
12. R. C. Gonzalez, R. E. Woods and S. L. Eddins, *Digital Image Processing Using MATLAB*, Pearson/PH, 2004. (Cheap Edition ?)
13. R. L. Graham, D. E. Knuth and O. Patashnik, *Concrete Mathematics : A Foundation for Computer Science*, Second Edition, Addison-Wesley, 1994. (Cheap Edition)
14. F. Halsall, *Computer Networking and the Internet*, Fifth Edition, Pearson/AW, 2005.
15. J. E. Hopcroft, R. Motwani and J. D. Ullman, *Introduction to Automata Theory, Languages, and Computation*, Second Edition, Addison-Wesley, 2001. (Cheap Edition)
16. X. Huang, A. Acero and H-W. Hon, *Spoken Language Processing : A Guide to Theory, Algorithm and System Development*, Prentice-Hall, 2001.
17. D. Jurafsky and J. H. Martin, *Speech and Language Processing : An Introduction to Natural Language Processing, Computational Linguistics and Speech Recognition*, Prentice-Hall, 2000.
18. B. W. Kernighan and R. Pike, *The Practice of Programming*, Addison-Wesley, 1999.

19. J. F. Kurose and K. W. Ross, *Computer Networking : A Top-Down Approach Featuring the Internet*, Third Edition, Pearson/AW, 2004.
20. J. Levine, T. Mason and D. Brown, *lex & yacc*, Second Edition, ORA, 1992.
21. A. V. Levitin, *Introduction to the Design and Analysis of Algorithms*, Second Edition, Pearson/AW, 2006.
22. W. Mao, *Modern Cryptography : Theory and Practice*, Prentice-Hall, 2003.
23. T. M. Mitchell, *Machine Learning*, McGraw-Hill, 1997.
24. J. O'Rourke, *Computational Geometry in C*, CUP, 2000.
25. L. L. Peterson and B. S. Davie, *Computer Networks : A Systems Approach*, Third Edition, Elsevier/MK, 2003.
26. F. P. Preparata and M. I. Shamos, *Computational Geometry : An Introduction*, Springer, 1993.
27. S. J. Russell and P. Norvig, *Artificial Intelligence : A Modern Approach*, Second Edition, Pearson/PH, 2002. (Cheap Edition ?)
28. R. Sedgewick, *Algorithms in C++, Parts 1-4 : Fundamentals, Data Structures, Sorting and Searching*, Third Edition, Pearson/AW, 2004. (Cheap Edition)
29. R. Sedgewick, *Algorithms in C++, Part 5 : Graph Algorithms*, Third Edition, Pearson/AW, 2004. (Cheap Edition)
30. L. G. Shapiro and G. C. Stockman, *Computer Vision*, Pearson/PH, 2001.
31. P. Shirley, *Fundamentals of Computer Graphics*, AK Peters, 2002. (Cheap Edition)
32. A. Silberschatz, P. B. Galvin and G. Gagne, *Operating System Concepts*, Seventh Edition, Wiley, 2004. (Cheap Edition)
33. W. Stallings , *Cryptography and Network Security : Principles and Practices*, Fourth Edition, Prentice-Hall, 2005. (Cheap Edition ?)
34. A. S Tanenbaum and A. S Woodhull, *Operating Systems Design and Implementation*, Third Edition, Pearson/PH, 2006. (Cheap Edition)

Miscellaneous Books

1. E. Balagurusamy, *Numerical Methods*, Tata McGraw-Hill, 1999. (Cheap Edition)
2. S. Boyd and L. Vandenberghe, *Convex Optimization*, CUP, 2004.
3. S. C. Chapra and R. P. Canale, *Numerical Methods for Engineers*, Fifth Edition, McGraw-Hill, 2006.
4. W. J. Cook, W. H. Cunningham, W. R. Pulleyblank and A. Schrijver, *Combinatorial Optimization*, Wiley, 1997.

5. W. H. Hayt, J. Kemmerly and S. M. Durbin, *Engineering Circuit Analysis*, Seventh Edition, McGraw-Hill, 2007. (To Appear)
6. J. Heywood, *Engineering Education : Research and Development in Curriculum and Instruction*, Wiley/IEEE, 2005.
7. W. C. Huffman and V. Pless, *Fundamentals of Error-Correcting Codes*, CUP, 2003.
8. J. W. S. Liu, *Real-Time Systems*, Prentice-Hall, 2000. (Cheap Edition)
9. C. McCormack and D. Jones, *Building a Web-based Education System*, Wiley, 1998.
10. D. Jordan and P. Smith, *Mathematical Techniques : An Introduction for the Engineering, Physical, and Mathematical Sciences*, Third Edition, OUP, 2002.
11. F. J. MacWilliams and N. J. A. Sloane, *The Theory of Error-Correcting Codes*, Elsevier/North-Holland, Eleventh Reprint, 1978/2003.
12. W. Newman and M. Lamming, *Interactive System Design*, Addison-Wesley, 1995.
13. V. Pless, *Introduction to the Theory of Error-Correcting Codes*, Third Edition, Wiley, 1998.
14. W. H. Press, B. P. Flannery, S. A. Teukolsky and W. T. Vetterling, *Numerical Recipes in C*, Second Edition, CUP, 1992/1998. (Cheap Edition)
15. W. H. Press, S. A. Teukolsky, W. T. Vetterling and B. P. Flannery, *Numerical Recipes in C++*, Second Edition, CUP, 2002. (Cheap Edition)
16. M. S. Sarma, *Introduction to Electrical Engineering*, OUP, 2000.
17. S. S. Sastry, *Introductory Methods of Numerical Analysis*, 3rd Edition, Prentice-Hall, 1998. (Cheap Edition)
18. B. Shneiderman, *Designing the User Interface*, Third Edition, Addison-Wesley, 1998.
19. R. J. Wilson and J. J. Watkins, *Graphs : An Introductory Approach*, Wiley, 1990.
20. L. A. Wolsey and G. L. Nemhauser, *Integer and Combinatorial Optimization*, Wiley, 1999.

Magazines

1. IEEE Spectrum Magazine.
2. IEEE Computer Magazine.
3. IEEE Circuits & Devices Magazine.
4. IEEE Design & Test Magazine.
5. IEEE Micro Magazine.

Journals and Transactions

1. IEEE Electron Device Letters.
2. IEEE Transactions on Electron Devices.
3. IEEE Journal of Solid State Circuits.
4. IEEE Transactions on Computers.
5. IEEE Transactions on CAD of ICs and Systems.
6. IEEE Transactions on Circuits and Systems : Volume I.
7. IEEE Transactions on Circuits and Systems : Volume II.
8. IEEE Transactions on VLSI Systems.
9. Proceedings of IEEE.

Conferences and Proceedings

1. DAC (<http://www.dac.com/>).
2. ASPDAC (<http://www.aspdac.com/>).
3. EuroDAC (*Discontinued since 1996 – replaced by DATE*).
4. DATE (<http://www.date-conference.com/>).
5. GLSVLSI (<http://www.sigda.org/glsvlsi/>).
6. ESSDERC/ESSCIRC (<http://www.essderc.org/>).
7. CODES + ISSS (<http://www.ida.liu.se/conferences/codes/>).
8. ICCAD (<http://www.iccad.com/>).
9. IEEE-IEDM (<http://www.his.com/~iedm/>).
10. IEEE-CICC (<http://www.ieee-cicc.org/>).

11. IEEE-SOCC (<http://www.ieee-socc.org/>).
12. ISLPED (<http://www.islped.org/>).
13. VLSI Design (<http://www.vlsiconference.com/>).
14. VDAT-VLSI (<http://www.vlsi-india.org/>).
15. ITC (<http://www.itctestweek.org/>).
16. ATS (*No fixed URL – changes every year*).
17. ISCA (*No fixed URL – changes every year*).
18. ACM Conferences List (<http://www.acm.org/events/>).
19. IEEE Conferences List (<http://ieeexplore.ieee.org/xpl/conferences.jsp>).
20. ACM SIGDA (<http://www.sigda.org/>).
21. IEEE Solid-State Circuits Society (<http://www.sscs.org/>).

The proceedings of these conferences are usually available from IEEE Press or ACM.